

# Practical Digital Design And Testing

By Parag K. Lala



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Parag Lala is the Mullins Chair He is well known for his research in the areas of VLSI system design and testing, self Dr. Parag K. Lala Professor Computer

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Parag K. Lala is the Cary and Lois Patterson Chair and Founding Chairman of Electrical Practical Digital Logic Design and Testing (Prentice

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